



PATENT APPLICATION

Group Art Unit: 2812

Docket No.: 127570

S. NIKMANESH

Examiner:

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Atsushi SANO et al.

Application No.: 10/574,571

Filed: April 28, 2006

For:

MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND SUBSTRATE PROCESSING APPARATUS

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In reply to the May 21, 2008 Restriction Requirement, Applicants provisionally elect Group I, claims 1-19.

Should the Examiner believe that anything further would be desirable in order to place this application in even better condition for allowance, the Examiner is invited to contact the undersigned at the telephone number set forth below.

Respectfully submitted,

Registration No. 27,075

Aaron L. Webb

Registration No. 56,930

JAO/HXT:amw

Date: June 13, 2008

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